Ref #	Hits	Search Query	DBs	Default Operato r	Plural s	Time Stamp
L1	163	latency with ((plural\$4 or multiple or more)adj3 (level\$1 or voltage))	US-PGPUB ; USPAT; USOCR; IBM_TDB	OR	OFF	2004/11/22 15:04
L2	5	I1 and (memory adj module)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2004/11/22 15:09
L3.	0	I1 same (cas or ras)	US-PGPUB ; USPAT; USOCR; IBM_TDB	OR	OFF	2004/11/22 15:09
L4	155	I1 and (memory )	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2004/11/22 15:09
L5	4	l1 and (cas or ras)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2004/11/22 15:11
L6	2	l1 and (pin adj count)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2004/11/22 15:11